

### **SSC7002EGS6**

### N-Channel Small Switching MOSFET with ESD Protection

### > Features

VDS	VGS	RDSON Typ.	ID	ESD
60)/	±20V	2R@10V	0.24	3kV
60V	±20V	3R@4V5	0.3A	SKV

### Description

This device is an N-Channel enhancement mode MOSFET, with low on-resistance, fast switching speed and low threshold voltage, it is ideal for portable equipment.

### Applications

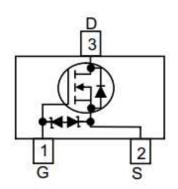
- Direct Logic-Level Interface:TTL/CMOS
- Drivers: Relays, Solenoids,Lamps, Hammers
- Display, Memories, Transistors, etc.
- Battery Operated System
- Solid-State Relays

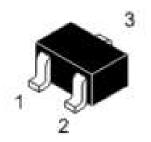
### > Ordering Information

Device	Package	Shipping
SSC7002EGS6	SOT23	3000/Reel

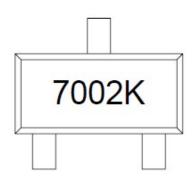
### Pin configuration

Top view





SOT23



Marking



## ➤ Absolute Maximum Ratings(T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit
$V_{DSS}$	Drain-to-Source Voltage	60	V
V <sub>GSS</sub>	Gate-to-Source Voltage	±20	V
I <sub>D</sub>	Continuous Drain Current a	0.3	Α
I <sub>DM</sub>	Pulsed Drain Current <sup>b</sup>	0.8	Α
P <sub>D</sub>	Power Dissipation <sup>c</sup>	0.83	W
P <sub>DSM</sub>	Power Dissipation <sup>a</sup>	0.35	W
TJ	Operation junction temperature	-55 to 150	℃
T <sub>STG</sub>	Storage temperature range	-55 to 150	°C

### ➤ Thermal Resistance Ratings(T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Typical	Maximum	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance <sup>a</sup>		357	°C 0M
Rejc	Junction-to-Case Thermal Resistance		159	°C/W

#### Note:

- a. The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper,in a still air environment with  $T_A$ =25°C. The value in any given application depends on the user is specific board design. The current rating is based on the t $\leq$  10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation  $P_D$  is based on  $T_{J(MAX)}$ =150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

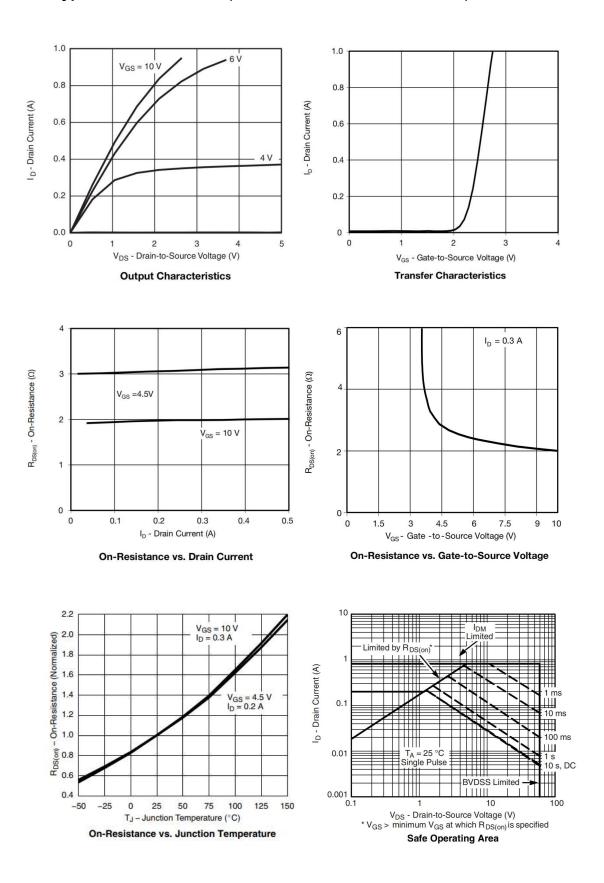


# ➤ Electronics Characteristics(T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
V(BR)DSS	Drain-Source Breakdown Voltage	VGS=0V,ID=10uA	=0V,ID=10uA 60			V
VGS (th)	Gate Threshold  Voltage	VDS=VGS,ID=250uA 1			2.5	V
Door \	Drain-Source On-	VGS=10V,ID=0.5A		2	6	R
R <sub>DS(on)</sub>	Resistance	VGS=4.5V,ID=0.05A		3	8	K
IDSS	Zero Gate Voltage  Drain Current	VDS=60V,VGS=0V			1	uA
Igss	Gate-Source leak	VGS=±20V,VDS=0V			±10	uA
<b>G</b> FS	Transconductance	VDS=10V,ID=0.2A		0.08		S
VsD	Forward Voltage	VGS=0V,IS=0.2A		0.7	1.3	V
Ciss	Input Capacitance			32		
Coss	Output Capacitance	VDS=25V, VGS=0V, f=1MHZ		7		pF
Cros	Reverse Transfer			2		
Crss	Capacitance			3		
T <sub>D(ON)</sub>	Turn-on delay time			4.2		
Tr	Rise Time	VGS=10V, VDS=20V, RG=20R,		3.8		ns
T <sub>D(OFF)</sub>	Turn-off delay time	RL=60R		22		
Tf	Fall Time			14		
Q <sub>G</sub>	Total Gate Charge			0.4		
Q <sub>G</sub> s	Gate Source Charge	VGS=10V, VDS=15V, ID=0.2A		0.1		nC
Q <sub>GD</sub>	Gate Drain Charge	<del></del> -		0.11		

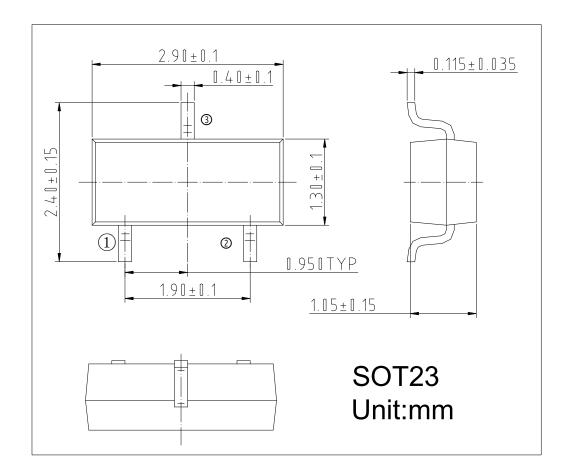


## > Typical Characteristics(T<sub>A</sub>=25°C unless otherwise noted)



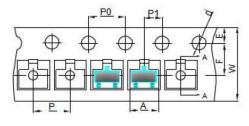
## Package Information

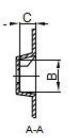




## SOT-23 Tape and reel

### SOT-23 Embossed Carrier Tape

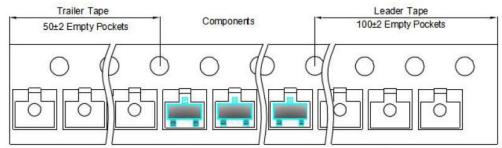




Dimensions are in millimeter										
Pkg type	Α	В	С	d	E	F	P0	Р	P1	W
SOT-23	3.15±0.1	2.77±0.1	1.22±0.1	Ø1.50	1.75±0.1	3.5±0.05	4.0±0.1	4.0±0.1	2±0.05	8±0.1

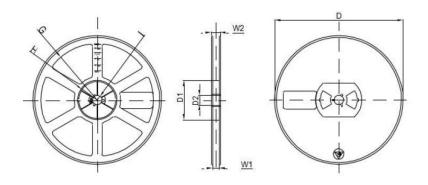


### SOT-23 Tape Leader and Trailer



SOT23 带尾(空封 40 格)、带头(空封 100 格)空封数

### SOT-23 Reel



Dimensions are in millimeter								
Reel Option	D	D1	D2	G	Н	ï	W1	W2
7"Dia	Ø178.00	Ø54±0.2	13.3±0.2	R79.00	R26.00	R6.50±0.2	9±0.5	12±0.5



### History Version

V1.0	Product Release	
V2.0	Update POD and Tape&Reel	2020-08-28
V3.0	Update Curve	2021-08-31

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